

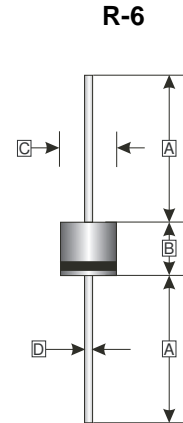
RoHS Compliant Product
A suffix of "-C" specifies halogen & lead-free

FEATURES

- Low Forward Voltage Drop
- High Current Capability
- High Reliability
- High Surge Current Capability

MECHANICAL DATA

- Case: R-6 Molded Plastic
- Epoxy: UL 94V-0 Rate Flame Retardant
- Lead: Axial Leads, Solderable Per MIL-STD-202, Method 208 Guaranteed
- Polarity: Color Band Denotes Cathode End
- High Temperature Soldering Guaranteed: 250°C/10s
.375" (9.5mm) Lead Lengths at 5 lbs., (2.3kg) Tension
- Mounting position: Any



REF.	Millimeter	
	Min.	Max.
A	25.4 REF.	
B	8.6	9.1
C	8.6	9.1
D	1.2	1.32

ORDER INFORMATION

Part Number	Type
HER601G~HER607G	Lead (Pb)-free
HER601G~HER607G-C	Lead (Pb)-free and Halogen-free

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

(Rating 25°C ambient temperature unless otherwise specified. Single phase half wave, 60Hz, resistive or inductive load.
For capacitive load, de-rate current by 20%)

Parameter	Symbol	Part Number							Unit
		HER601G	HER602G	HER603G	HER604G	HER605G	HER606G	HER607G	
Maximum Repetitive Peak Reverse Voltage	V_{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS Voltage	V_{RMS}	35	70	140	280	420	560	700	V
Maximum DC Blocking Voltage	V_{DC}	50	100	200	400	600	800	1000	V
Maximum Average Forward Rectified Current @0.375" (9.5mm) lead length, $T_A=50^\circ\text{C}$	$I_{F(AV)}$	6							A
Peak Forward Surge Current, 8.3ms single half sine-wave superimposed on rated load (JEDEC Method)	I_{FSM}	250							A
Maximum Instantaneous Forward Voltage @ $I_F=6A$	V_F	1		1.3		1.7			V
Maximum DC Reverse Current @Rated DC Blocking Voltage	$T_A=25^\circ\text{C}$	10							μA
	$T_A=125^\circ\text{C}$	200							
Maximum Reverse Recovery Time ¹	T_{rr}	50				75			nS
Typical Junction Capacitance ²	C_J	100				65			pF
Operating & Storage Temperature Range	T_J, T_{STG}	150, -55~150							$^\circ\text{C}$

Notes:

1. Reverse Recovery Test Condition: $I_F=0.5A$, $I_R=1A$, $I_{RR}=0.25A$.
2. Measured at 1MHz and applied reverse voltage of 4V D.C.

RATINGS AND CHARACTERISTIC CURVES HER601G~HER604G

FIG.1- REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM

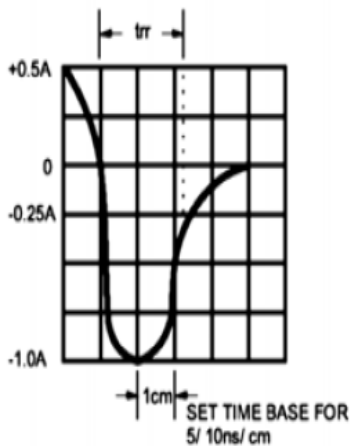
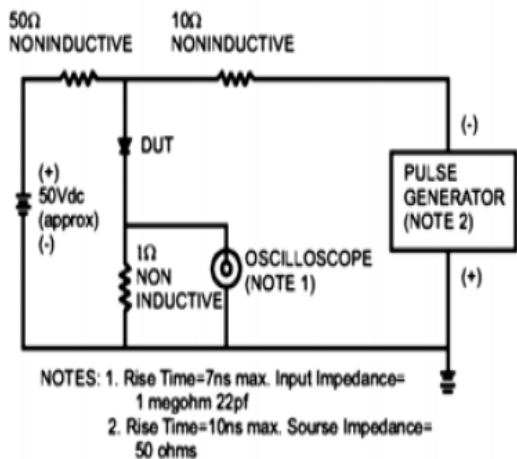


FIG.2- MAXIMUM FORWARD CURRENT DERATING CURVE

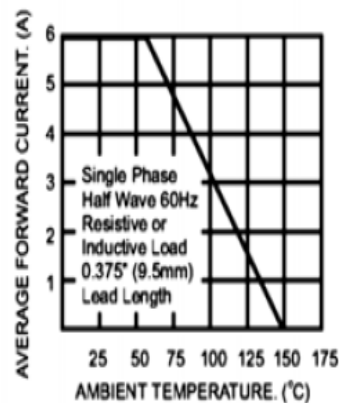


FIG.3- TYPICAL REVERSE CHARACTERISTICS

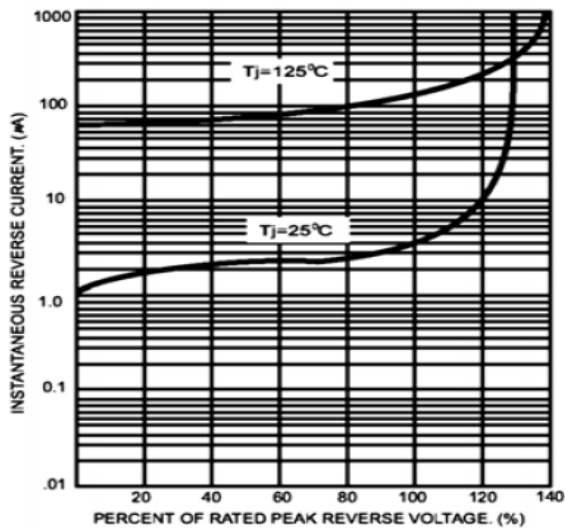


FIG.4- TYPICAL FORWARD CHARACTERISTICS

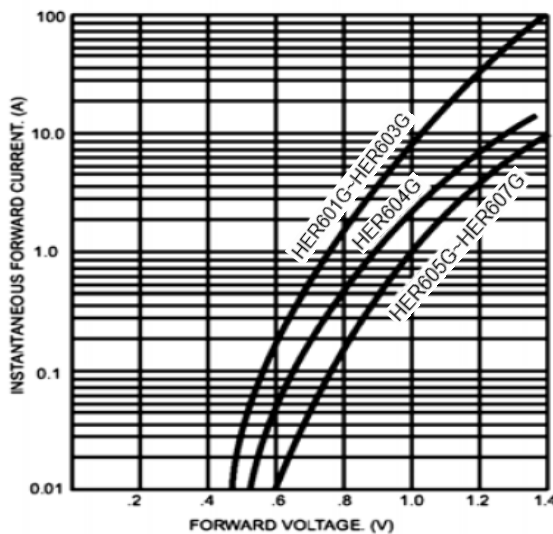


FIG.5- MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

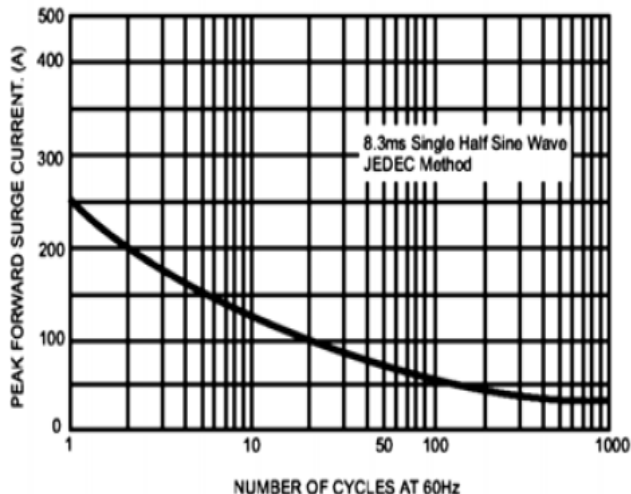


FIG.6- TYPICAL JUNCTION CAPACITANCE

